

### Unit 9

#### Implementing Combinational Functions with Karnaugh Maps

#### Outcomes

9.2

- I can use Karnaugh maps to synthesize combinational functions with several outputs
- I can determine the appropriate size and contents of a memory to implement any logic function (i.e. truth table)

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## **Covering Combinations**

- A minterm corresponds to ("covers") 1 combination of a logic function
- As we \_\_\_\_\_\_ variables from a product term, more combinations are covered
  - The product term will evaluate to true

of the removed variables value (i.e. the term is independent of that variable)

$\mathbf{F} = \mathbf{WX'YZ} = \mathbf{m11}$				
w	Х	Y	Z	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

	$\mathbf{F} = \mathbf{W}\mathbf{X'Z}$					
	= 1	m9+1	n11			
W	Х	Y	z	F		
0	0	0	0	0		
0	0	0	1	0		
0	0	1	0	0		
0	0	1	1	0		
0	1	0	0	0		
0	1	0	1	0		
0	1	1	0	0		
0	1	1	1	0		
1	0	0	0	0		
1	0	0	1	1		
1	0	1	0	0		
1	0	1	1	1		
1	1	0	0	0		
1	1	0	1	0		
1	1	1	0	0		
1	1	1	1	0		

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## **Covering Combinations**

0

0

0

0

Λ

0

n

1

• The more variables we can remove the more

a single

product term covers

- Said differently, a small term will cover (or expand to) more combinations
- The smaller the term, the smaller the \_\_\_\_\_
  - We need fewer \_\_\_\_\_ to check for multiple combinations
- For a given function, how can we find these smaller terms?

$\mathbf{F} = \mathbf{X'Z}$ $= m1 + m3 + m9 + m11$				= m0+n	F n1+m2+	T = X -m3+m8	K' 3+m9+n	n10+m11	
х	Y	z	F	]	W	Х	Y	Z	F
0	0	0	0	-	0	0	0	0	1
0	0	1	1		0	0	0	1	1
0	1	0	0		0	0	1	0	1
0	1	1	1		0	0	1	1	1
1	0	0	0		0	1	0	0	0
1	0	1	0		0	1	0	1	0
1	1	0	0		0	1	1	0	0
1	1	1	0		0	1	1	1	0
0	0	0	0		1	0	0	0	1
0	0	1	1		1	0	0	1	1
0	1	0	0		1	0	1	0	1
0	1	1	1		1	0	1	1	1
1	0	0	0		1	1	0	0	0
1	0	1	0		1	1	0	1	0
1	1	0	0		1	1	1	0	0
1	1	1	0		1	1	1	1	0

## KARNAUGH MAPS

A new way to synthesize your logic functions



## **Logic Function Synthesis**

9.6

- Given a function description as a T.T. or sum of minterm (product of maxterm) form, how can we arrive at a circuit implementation or equation (i.e. perform logic synthesis)?
- Methods
  - Minterms / maxterms
    - Use \_\_\_\_\_\_ to find minimal 2-level implementation
  - Karnaugh Maps [we will learn this one now]
    - Graphical method amenable to human \_\_\_\_\_\_ inspection and can be used for functions of up to \_\_\_\_ variables (but becomes large and unwieldy after just \_\_\_\_\_ variables)
  - Quine-McCluskey Algorithm (amenable to computer implementations)
  - Others: Espresso algorithm, Binary Decision Diagrams, etc.

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- If used correctly, will always yield a minimal, implementation
  - There may be a more minimal 3-level, 4-level, 5level... implementation but K-maps produce the minimal two-level (SOP or POS) implementation
- Represent the truth table graphically as a series of adjacent \_\_\_\_\_\_ that allows a human to see where variables can be removed

## **Gray Code**

9.8

- Different than normal binary ordering
- Reflective code
  - When you add the (n+1)<sup>th</sup> bit, reflect all the previous n-bit combinations
- Consecutive code words differ by only 1-bit



## Karnaugh Map Construction

- Every square represents 1 input combination
- Must label axes in Gray code order
- Fill in squares with given function values





G(w,x,y,z)=m1+m2+m3+m5+m6+m7+m9+ m10+m11+m14+m15

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F(x,y,z)=m1 + m4 + m5 + m6

4 Variable Karnaugh Map



#### Karnaugh Maps

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1

YZ WZ	<sup>K</sup> 00	01	11	10
00	• 0	<sup>4</sup> 0	<sup>12</sup> 0	<sup>8</sup> 0
01	<sup>1</sup> 1	<sup>5</sup> 1	<sup>13</sup> ()	<sup>9</sup> 1
11	<sup>3</sup> 1	<sup>7</sup> 1	15 1	<sup>11</sup> 1
10	<sup>2</sup> 1	<sup>6</sup> 1	14 1	10 1



- Squares with a '1' represent minterms that must be included in the SOP solution
- Squares with a '0' represent maxterms that must be included in the POS solution





 Groups (of 2, 4, 8, etc.) of adjacent 1's will always simplify to smaller product term than just individual minterms



**3** Variable Karnaugh Map

9.13

- Adjacent squares differ by 1-variable
  - This will allow us to use T10 = AB + AB'= A or T10' = (A+B')(A+B) = A



9.14

- 2 adjacent 1's (or 0's) differ by only one variable
- 4 adjacent 1's (or 0's) differ by two variables
- 8, 16, ... adjacent 1's (or 0's) differ by 3, 4, ... variables
- By grouping adjacent squares with 1's (or 0's) in them, we can come up with a simplified expression using T10 (or T10' for 0's)



## K-Map View of the Theorems

• The 2 & 3 variable theorems used to simplify expressions can be illustrated using K-Maps.

T9: Covering X + XY = X

T10: Combining XY + XY' = X

T11: Consensus XY + X'Z + ZY = XY + X'Z

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X "covers" XY so XY not needed XY and XY' can be combined to form X

Don't need ZY if you have X'Z and XY

## K-Map Grouping Rules

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- Cover the 1's [=on-set] or 0's [=off-set] with groups as possible, but make those groups \_\_as possible
  - Make them as large as possible even if it means "covering" a 1 (or 0) that's already a member of another group
- Make groups of \_\_\_\_\_, ... and they must be rectangular or square in shape.
- Wrapping is legal



#### **Group These K-Maps**











 Cover the remaining '1' with the largest group possible even if it "reuses" already covered 1's

#### Karnaugh Maps

- Groups can wrap around from:
  - Right to left
  - Top to bottom
  - Corners





## **Group** This



## **K-Map Translation Rules**

- When translating a group of 1's, find the variable values that are constant for each square in the group and translate only those variables values to a product term
- Grouping 1's yields SOP
- When translating a group of O's, again find the variable values that are constant for each square in the group and translate only those variable values to a sum term
- Grouping O's yields POS



#### Karnaugh Maps (SOP)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} =$ 



#### Karnaugh Maps (SOP)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1







#### Karnaugh Maps (SOP)

W	Χ	Υ	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} = \mathbf{Y} + \mathbf{W'Z} + \dots$ 



#### Karnaugh Maps (SOP)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} = \mathbf{Y} + \mathbf{W'Z} + \mathbf{X'Z}$ 



## Karnaugh Maps (POS)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} =$ 



#### Karnaugh Maps (POS)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} = (\mathbf{Y} + \mathbf{Z})$ 



#### Karnaugh Maps (POS)

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



 $\mathbf{F} = (\mathbf{Y} + \mathbf{Z})(\mathbf{W'} + \mathbf{X'} + \mathbf{Y})$ 

- Groups can wrap around from:
  - Right to left
  - Top to bottom
  - Corners



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#### **Exercises**





F<sub>SOP</sub>=



P(x,y,z)=m2+m3+m5+m7



F<sub>POS</sub>=



#### No Redundant Groups





## **Multiple Minimal Expressions**

For some functions,
groupings
exist which will lead to
alternate minimal
\_\_\_\_\_...Pick one



#### Karnaugh Maps Beyond 4 Variables

- Recall, K-Maps require an adjacency for each variable
  - To see the necessary adjacencies, 5 and 6 variable K Maps can be thought of in three dimensions
- Can we have 7-variable K-Maps?
  - No! We would need to see 7 adjacencies per square and we humans cannot visualize 4 dimensions
- Other computer-friendly minimization algorithms
  - Quine-McCluskey
    - Still exponential runtime
    - Minimization is NP-hard problem
  - Espresso-heuristic Minimizer
    - Achieves "good" minimization in far less time (may not be absolute minimal)

**5 Variable K-Maps** 

V=0

V=1

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6 Variable K-Maps

## **DON'T CARE OUTPUTS**



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- Sometimes there are certain input combinations that are illegal (due to physical or other external constraints)
- The outputs for the illegal inputs are "don't-cares"
  - The output can either be 0 or 1 since the inputs can never occur
  - Don't-cares can be included in groups of 1 or groups of 0 when grouping in K-Maps
  - Use them to make as big of groups as possible

Use 'Don't care' outputs as wildcards (e.g. the blank tile in Scrabble<sup>™</sup>). They can be either 0 or 1 whatever helps make bigger groups to cover the ACTUAL 1's

## Invalid Input Combinations

- Given intermediate functions F1 and F2, how could you use AND, OR, NOT to make G
- Notice certain F1,F2 combinations never occur in G(x,y,z)...what should we make their output in the T.T.

Х	Y	Z	F1	F2	G
0	0	0	0	0	0
0	0	1	1	0	1
0	1	0	1	0	1
0	1	1	1	0	1
1	0	0	1	0	1
1	0	1	1	0	1
1	1	0	1	0	1
1	1	1	1	1	0



F1	F2	G
0	0	
0	1	
1	0	
1	1	



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## Invalid Input Combinations

- An example of where Don't-Cares may come into play is Binary Coded Decimal (BCD)
  - Rather than convert a decimal number to unsigned binary (i.e. summing increasing powers of 2) we can represent each decimal digit as a separate group of 4-bits (with weights 8,4,2,1 for each group of 4 bits)
  - Combinations 1010-1111 cannot occur!



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#### Don't Care Example

				1	、D8D	4				
D8	D4	D2	D1	GT6	D2D1	00	01	11	10	
0	0	0	0	0	0.0	0	4		8	
0	0	0	1	0	UU	0	U	u	1	GT6 <sub>cop</sub> =
0	0	1	0	0	01	1	<sup>5</sup> 0	13 d	9 1	o i osop
0	0	1	1	0	•	2	7	15	11	
0	1	0	0	0	11	0	1	d	<sup>n</sup> d	
0	1	0	1	0	-	2	6	14	10	
0	1	1	0	0	10	0	0	d	d	
0	1	1	1	1	L					
1	0	0	0	1	<b>_ D8</b>	D4				
1	0	0	1	1	D2D1	00	01	11	10	7
1	0	1	0	d	00	0	<sup>4</sup> 0	<sup>12</sup> d	<sup>8</sup> 1	
1	0	1	1	d		1	5	13	9	GT6 <sub>POS</sub> =
1	1	0	0	d	01	0	0	d	1	
1	1	0	1	d		3	7	15	11	-
1	1	1	0	d	11	0	I	d	d	
1	1	1	1	d	10	<sup>2</sup> 0	<sup>6</sup> 0	<sup>14</sup> d	<sup>10</sup> d	

#### Don't Cares



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w	X	Y	z	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	d
1	0	1	1	d
1	1	0	0	d
1	1	0	1	d
1	1	1	0	d
1	1	1	1	d

\_

#### Don't Cares

F	_						
0	-				Y	ou ca	n use "d's"
1					W	hen gi	ouping 0's
1					6	ind co	nverting to
1							:
0		YZ WZ	x 00	01	11	10	1
1		`	0	4	12	8	
1		00		0	u		
1		01	1 1	5	13 d	9 1	
0		Ŭ1	2	7	15	11	
1		11	1	1	d	d d	
d			2	6	14	10	
d		10	1	1	d	d	
d			<u> </u>	<b>F</b> =	Y+Z		

**US** 

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W	Χ	Υ	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	d
1	0	1	1	d
1	1	0	0	d
1	1	0	1	d
1	1	1	0	d
1	1	1	1	d

## A GENERAL, COMBINATIONAL CIRCUIT DESIGN PROCESS



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# Combinational Design Process

- Understand the problem
  - How many input bits and their representation system
  - How many output bits need be generated and what are their representation
  - Draw a block diagram
- Write a truth table
- Use a K-map to derive an equation for EACH output bit
- Use the equation to draw a circuit for EACH output bit, letting each circuit run in parallel to produce their respective output bit



<b>X</b> <sub>2</sub>	<b>X</b> <sub>1</sub>	X <sub>0</sub>	<b>Z</b> <sub>2</sub>	<b>Z</b> <sub>1</sub>	<b>Z</b> <sub>0</sub>
0	0	0	1	1	1
0	0	1	0	0	0
0	1	0	0	0	1
0	1	1	0	1	0
1	0	0	0	1	1
1	0	1	1	0	0
1	1	0	1	0	1
1	1	1	1	1	0





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# Designing Circuits w/ K-Maps

- Given a description...
  - Block Diagram
  - Truth Table
  - K-Map for each output bit (each output bit is a separate function of the inputs)
- 3-bit unsigned decrementer (Z = X-1)
  - If X[2:0] = 000 then Z[2:0] = 111, etc.



## 3-bit Number Decrementer

X <sub>2</sub>	<b>X</b> <sub>1</sub>	X <sub>0</sub>	<b>Z</b> <sub>2</sub>	<b>Z</b> <sub>1</sub>	Z <sub>0</sub>
	0	0	1	1	1
)	0	1	0	0	0
0	1	0	0	0	1
0	1	1	0	1	0
1	0	0	0	1	1
1	0	1	1	0	0
1	1	0	1	0	1
1	1	1	1	1	0



 $Z_2 = X_2 X_0 + X_2 X_1 + X_2 X_1 X_0'$ 

 $Z_1 = X_1'X_0' + X_1X_0$ 

 $Z_0 = X_0'$ 

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## **Squaring Circuit**

- Design a combinational circuit that accepts a 3-bit number and generates an output binary number equal to the square of the input number. (B = A<sup>2</sup>)
- Using 3 bits we can represent the numbers from to \_\_\_\_\_\_\_.
- The possible squared values range from \_\_\_\_\_\_ to
- Thus to represent the possible outputs we need how many bits? \_\_\_\_\_

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#### **3-bit Squaring Circuit**

	]	[nput	S			Out	puts				<b>A</b> 2	A1	
Α	$A_2$	$A_1$	$A_0$	B <sub>5</sub>	$B_4$	B <sub>3</sub>	$B_2$	$B_1$	B <sub>0</sub>	$B = A^2$	AO	00	0
				_							0	0	2
											U		
												1	3
											1		
												В	5 =
												2A1	
											AO	00	0
												0	2
											U		
												1	3
												I	<b>3</b> 4 =







#### **3-bit Squaring Circuit**





If time permits...

### FORMAL TERMINOLOGY FOR KMAPS

## Terminology

- Implicant: A product term (grouping of 1's) that covers a subset of cases where F=1
  - the product term is said to "imply" F because if the product term evaluates to '1' then F='1'
- Prime Implicant: The largest grouping of 1's (smallest product term) that can be made
- Essential Prime Implicant: A prime implicant (product term) that is needed to cover all the 1's of F



#### **Implicant Examples**

W	Χ	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1





#### Implicant Examples

	W	X	Y	Z	F
_	0	0	0	0	0
	0	0	0	1	1
	0	0	1	0	0
	0	0	1	1	1
	0	1	0	0	0
	0	1	0	1	1
	0	1	1	0	0
	0	1	1	1	1
	1	0	0	0	0
	1	0	0	1	0
	1	0	1	0	1
	1	0	1	1	1
	1	1	0	0	0
	1	1	0	1	0
	1	1	1	0	1
	1	1	1	1	1





#### Implicant Examples

W	X	Y	Ζ	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
 1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1



An essential prime implicant (largest grouping possible, that must be included to cover all 1's)



#### Implicant Examples





#### Implicant Examples





#### **Implicant Examples**

W	Χ	Y	Ζ	F					
0	0	0	0	0					
0	0	0	1	1					
0	0	1	0	1					
0	0	1	1	1					
0	1	0	0	0		YZ WX 00	00 01	11	10
0	1	0	1	1		0	$\int \frac{4}{10}$	12	8
0	1	1	0	1	An implicant, but not			0	0
0	1	1	1	1	a PRIME implicant	01	1 1	$13 \\ 0$	1
1	0	0	0	0	because it is not as	3		15	
1	0	0	1	1	(should expand to	11	1 1	1	
1	0	1	0	1	combo's 3 and 7)	2	6	14	10
1	0	1	1	1		10	1 1	1	
1	1	0	0	0	•				
1	1	0	1	0		K			V ALL I
1	1	1	0	1	An essential prime	implicant		A	n essential prim

implicant

An essential prime implicant (largest grouping possible, that must be included to cover all 1's)

1

1

1

## K-Map Grouping Rules

9.56

- Make groups (implicants) of 1, 2, 4, 8, ... and they must be rectangular or square in shape.
- Include the minimum number of essential prime implicants
  - Use only *essential* prime implicants (i.e. as few groups as possible to cover all 1's)
  - Ensure that you are using *prime* implicants (i.e. Always make groups as large as possible reusing squares if necessary)



Informational: You won't be asked to perform 5- or 6-variable K-Maps

#### **5- & 6-VARIABLE KMAPS**

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## 5-Variable K-Map

- If we have a 5-variable function we need a 32-square KMap.
- Will an 8x4 matrix work?
  - Recall K-maps work because adjacent squares differ by 1-bit
- How many adjacencies should we have for a given square?
- \_\_\_!! But drawn in 2 dimensions we can't have \_\_\_\_ adjacencies.



## 5-Variable Karnaugh Maps

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 To represent the 5 adjacencies of a 5-variable function [e.g. f(v,w,x,y,z)], imagine two 4x4 K-Maps stacked on top of each other

Adjacency across the two maps



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#### 6-Variable Karnaugh Maps

 6 adjacencies for 6-variables (Stack of four 4x4 maps)





#### 7-Variable K-maps and Other Techniques

- Can we have 7-variable K-Maps?
- No! We would need to see 7 adjacencies per square and we humans cannot visualize 4 dimensions
- Other computer-friendly minimization algorithms
  - Quine-McCluskey
    - Still exponential runtime
    - Minimization is NP-hard problem
  - Espresso-heuristic Minimizer
    - Achieves "good" minimization in far less time (may not be absolute minimal)



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