

EE109: Introduction to Embedded Systems
Fall 2023 – Quiz 1
09/26/23, 7PM – 8:15PM

[Complete all the information in the box below.]

Name: _____			
Student ID: _____			
Email: _____@usc.edu			
Lecture section (Circle One):			
Weber	Redekopp	Puvvada	Weber
9:30 a.m.	11 a.m.	12:30 p.m.	2 p.m.

Ques.	Your score	Max score	Recommended Time
1		8	8 min.
2		10	10 min.
3		12	15 min.
4		6	7 min.
5		14	35 min.
Total		50	75 min.

Calculators are ONLY allowed on Question 3 – Analog/Resistive Circuits.
 Using them on any other problem is an academic integrity violation.

Only work on this exam will be graded (no work on scratch paper will be considered).

Do NOT write in the upper corner with the QR / Page number code.

1. (8 pts.) Number Systems

1.1. Convert **11001101** binary to unsigned **decimal**: _____

1.2. Convert **91 decimal** to (**unsigned**) **binary** (use exactly 8 bits):

0b_____

1.3. Convert **1101101.101** unsigned **binary** to **hexadecimal**:

_____ hexadecimal

A market stocks **36** different kinds of vegetables and **14** kinds of fruits.

1.4. If we wanted to assign a unique binary number to JUST the 36 vegetables how many bits would this require? _____

The market wants to assign fixed-size, unique binary numbers to each type of vegetable and fruit (i.e. fixed size means codes for fruits and vegetables should be the same number of bits). However, it wants to differentiate vegetables and fruits quickly by using the **MOST-SIGNIFICANT bit** to identify **vegetables** (i.e. **MSB=0**) from **fruits** (i.e. **MSB = 1**).

1.5. What is the minimum number of bits required to represent vegetables and fruits to meet this new system? _____

2. (10 pts.) **Bit Manipulations**. Complete the following single-line statements to perform the desired operation stated in the line(s) above the blanks. Assume a standard bit numbering (**bit 7** is the MSB, **bit 0** is the LSB = Least Significant Bit). You may NOT change the structure or code given.

2.1. Turn on (set to 1), bit **6** and **3** of PORTB without affecting other bits of PORTB.

PORTB _____;

2.2. Turn off (clear to 0), bit **1** of DDRC without affecting other bits of DDRC.

DDRC _____;

2.3. Assume bit **4**, **3**, and **2** of Group D are already configured to be inputs.

Complete the if statement to be true **ONLY** if

bit 4 is 1 (high voltage) and bit 3 and 2 are 0 (low voltage).

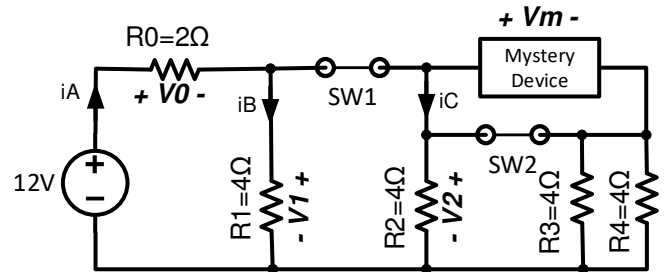
if(_____) {...};

3. (12 pts.) **Resistive Circuits.** Examine the circuit below and to the right and then answer the questions. Notice a.) all resistors labelled R_x are 4 ohms and b.) the mystery device which could be a resistor, wire, or open-circuit.

Show work for potential partial credit. A calculator may be used for this problem only.

For 3.1-3.4, determine and write **T** (for true) or **F** (for false) assuming **SW1** and **SW2** are **closed**.

- 3.1. ____ **T / F**: If the mystery device is an **open-circuit**, then $i_A = i_B + i_C$.
- 3.2. ____ **T / F**: If the mystery device is a **resistor** with **non-zero** resistance, then V_m will be **0**.
- 3.3. ____ **T / F**: If the mystery device is a **wire**, $V_2 = 12V - V_0$
- 3.4. ____ **T / F**: Regardless of what the mystery device is, **R1, R2, R3, and R4** are in **parallel**.



For 3.5-3.7, assume the mystery device is an OPEN CIRCUIT.

- 3.5. (2 pts.) With **SW1** and **SW2** **open**, solve for the voltage **V1** (round to 2 decimal places, if needed).

$$V_1 = \text{_____} \text{ V}$$

- 3.6. (2 pts.) Now with **SW1** **closed** and **SW2** **open**, solve for the voltage **V1** (round to 2 decimal places, if needed).

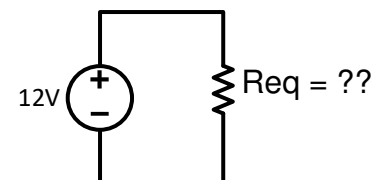
$$V_1 = \text{_____} \text{ V}$$

- 3.7. (2 pts.) Now with **SW1** **closed** and **SW2** **closed**, solve for the voltage **V1** (round to 2 decimal places, if needed).

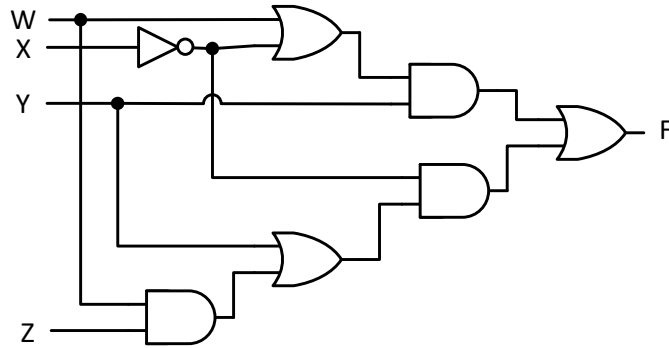
$$V_1 = \text{_____} \text{ V}$$

- 3.8. (2 pt.) If **SW1** **closed**, **SW2** **is back to open**, and assuming the **mystery device is a 6 ohm resistor**, what is the equivalent resistance of all resistors (round to **1 decimal place** if necessary).

$$R_{eq} = \text{_____} \text{ ohms}$$



4. (6 pts.) **Logic Circuits:** Consider the circuit shown below and reproduced at the bottom for markup.



Questions:

4.1) Given $\{W, X, Y, Z\} = 1, 0, 1, 0$ respectively, what value will **F** output? _____

4.2) How many levels of logic is this circuit? _____

4.3) It is possible to make the output, F, produce a `1` by assigning **only 2 of the 4 inputs** to specific values (i.e. the other two bit values don't matter). Two such pairs exist; find one and list them below.

First bit name: ___ First bit value: ___ Second bit name: ___ Second bit value: ___

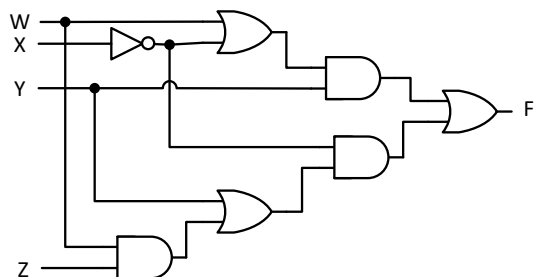
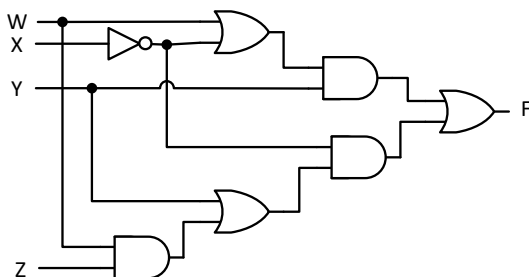
4.4) It is possible to make the output, F, produce a `0` by assigning **only 2 of the 4 inputs** to specific values (i.e. the other two bit values don't matter). Two such pairs exist; find one but you may NOT use the exact same two bits as you used in 4.3 above. List your answer below.

First bit name: ___ First bit value: ___ Second bit name: ___ Second bit value: ___

4.5) By assigning only 2 of the 3 inputs W, Y, and/or Z to specific values, we can make the output, F, ALWAYS equal/match **not X**, (i.e. when $X=0$, F will be 1 and when $X=1$, F will be 0. Find the two inputs (from W, Y, and Z) and their values. List your answer below.

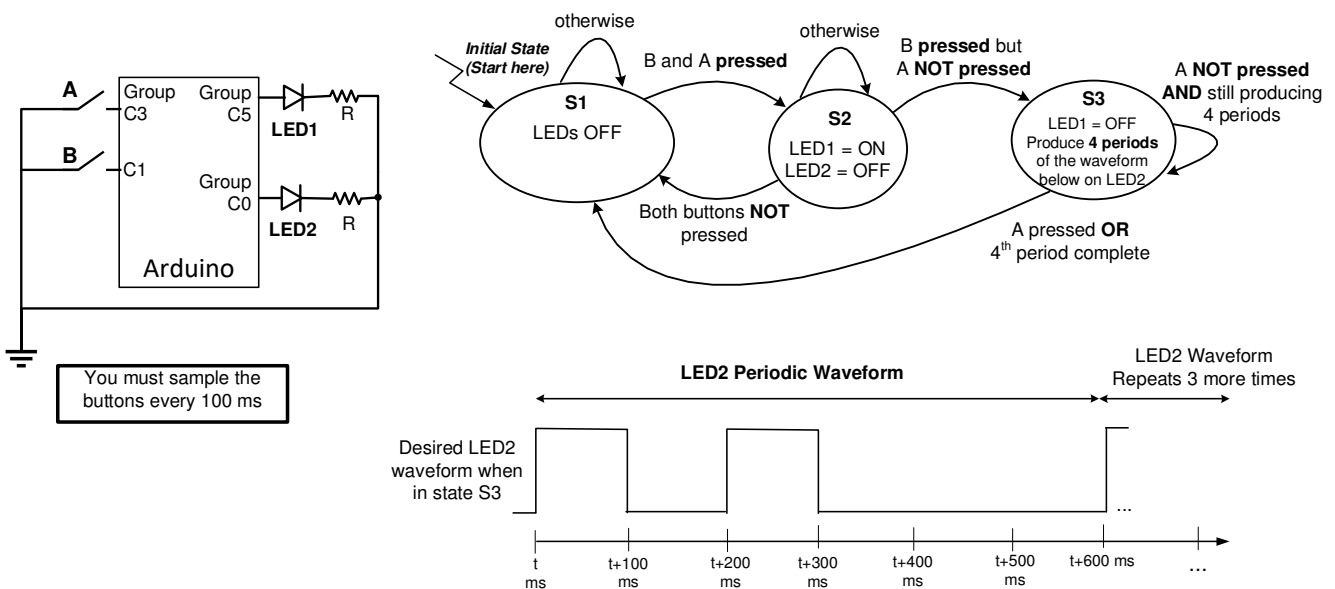
First bit name: ___ First bit value: ___ Second bit name: ___ Second bit value: ___

Repeated drawings for your own scratch work and annotation (will not be graded):



5. (14 pts.) **READ THE ENTIRE PAGE before solving.** Using the Arduino circuit below (exactly as drawn) with **two buttons (A and B) on group C, bit 3 and 1, respectively, and two LEDs (LED1 and LED2) on group C, bit 5 and 0, respectively, complete** the Arduino C-code program on the following page that implements the behavior described below.

Implement the software state machine specified in the state diagram below. In the S3 state, LED1 is OFF and **4 periods** of the waveform pattern should be generated on **LED2**. If A is pressed during generation of the 4 periods, immediately stop and transition to S1. Otherwise, transition to S1 immediately after the 4th period completes **fully**. Review the state diagram for the specification of how the S1 and S2 should work and what actions should be taken in those state. You must sample the input buttons every **100ms** and make the appropriate state transition. Most of the code is given on the next page. Your task is to fill in the missing blanks on the next page with the correct code.



Important Requirements

- The code separates Next State Transition Logic and Output Logic into separate if statements. You must use this approach and cannot alter the code structure.
- The **bit positions for buttons: A and B and outputs: LED1 and LED2 are defined at the start of the program.** These can be used wherever needed in the program.
- The program must **check/sample the button inputs every 100 ms** and update the state appropriately.
- You may **NOT add other DELAY** (e.g. `_delay_ms()`) statements than the one provided.
- You may **NOT alter the code given**, but may only fill in the blanks shown.
- You need not worry about debouncing the button presses.

Complete your code on the page below!

